



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Kevin M. Conley et al.

Title: Management of Non-Volatile Memory Systems Having Large Erase Blocks

Application No.: 10/749,831 Filing Date: December 30, 2003

Examiner: Unassigned Group Art Unit: 2186

Docket No.: SNDK.247US0 Conf. No.: 9380

Certificate of Mailing Under 37 CFR 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450,
on May 10, 2004

Mary S. Buogni
Signature

Mail Stop PGPUB Drawings
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Dear Sir:

Applicants submit twelve (12) sheets of formal drawings, consisting of Figures 1, 2, 3, 4, 5, 6, 7A, 7B, 8, 9, 10, 11A, 11B, 12A, 12B, 12C, 13A, 13B, 13C, 14, 15A, 15B, 16, 17, 18, 19 and 20, in the above-named application. If there are any questions regarding these drawings, please call the undersigned.

Respectfully submitted,

Gerald P. Parsons
Gerald P. Parsons
Reg. No. 24,486

May 10, 2004
Date

PARSONS HSUE & DE RUNTZ LLP
655 Montgomery Street, Suite 1800
San Francisco, CA 94111
(415) 318-1160 (main)
(415) 318-1163 (direct)
(415) 693-0194 (fax)